

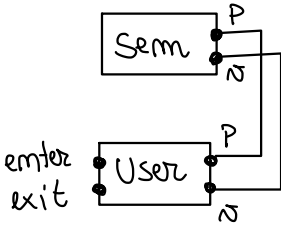
LCD (11/03/2024)

CCS

→ Syntax

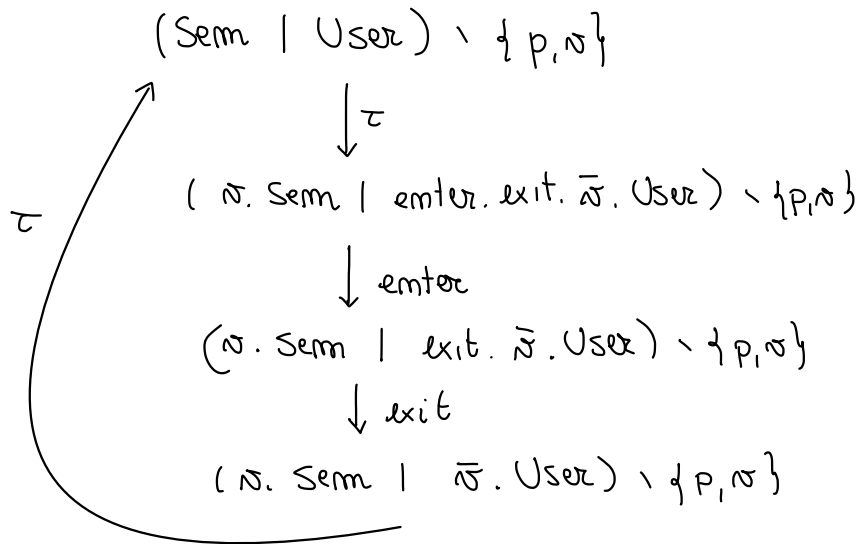
→ Operational behaviour (via syntax driven rules)

* Example two processes



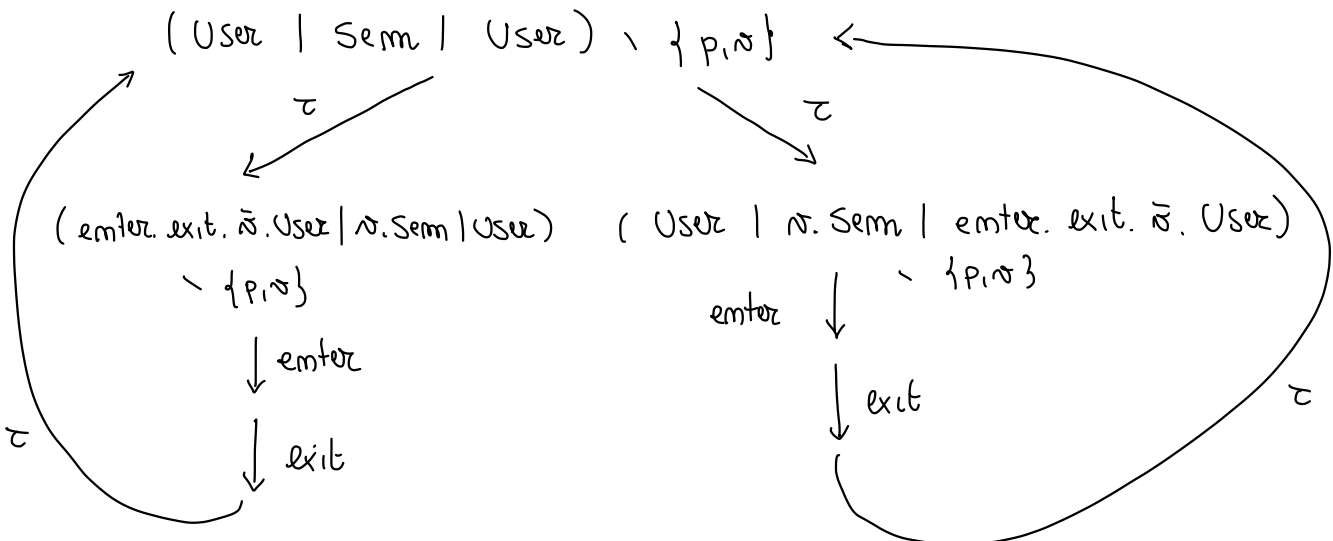
$$\text{Sem} = p. \bar{n}. \text{Sem}$$

$$\text{User} = \bar{p}. \text{enter}. \text{exit}. n. \text{User}$$



$$\text{Sem} = p. \bar{n}. \text{Sem}$$

$$\text{User} = \bar{p}. \text{enter}. \text{exit}. n. \text{User}$$



$$\text{Sem} = p. \sigma. \text{Sem}$$

$$\text{User} = \tau. \bar{p}. \underbrace{\text{enter} \cdot \tau \cdot \text{exit}}_{\text{critical section}} \cdot \bar{\sigma}. \text{User}$$

$$\boxed{(\text{User} \mid \text{Sem} \mid \text{User}) \setminus \{p, \sigma\}} \approx \text{Spec}$$

• •
 enter exit

enter exit enter exit ...

$$\text{Spec} = \text{enter} \cdot \text{exit} \cdot \text{Spec}$$

(~~enter . enter~~ exit . exit)

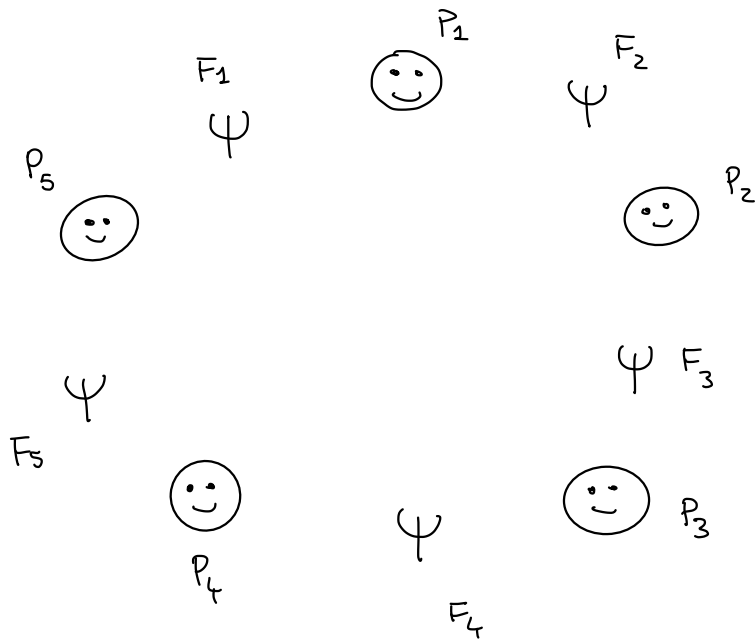
Exercise : Semaphore which allows at most k processes at the same time in the critical section

$$k=2 \quad \left\{ \begin{array}{l} \text{Sem}_2 = p. \text{Sem}_1 \\ \text{Sem}_1 = p. \text{Sem}_0 + \bar{\sigma}. \text{Sem}_2 \\ \text{Sem}_0 = \bar{\sigma}. \text{Sem}_1 \end{array} \right.$$

Implement the above behaviour by using Sem as a module

$$\boxed{\text{Sem} \mid \text{Sem}} \approx \boxed{\text{Sem}_2}$$

* Dining Philosophers



philosophers

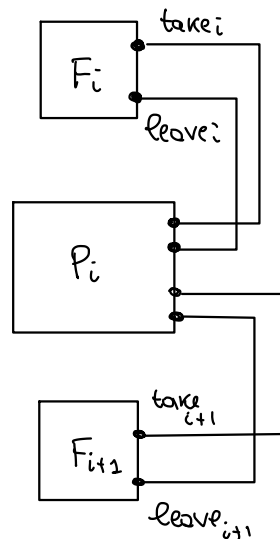
- think
- eat

P_i needs

F_i & F_{i+1}

$$F_i = \text{take}_i \cdot \text{leave}_i \cdot F_i$$

$$P_i = \text{think} \cdot \overline{\text{take}_i} \cdot \overline{\text{take}_{i+1}} \cdot \text{eat} \cdot \overline{\text{leave}_i} \cdot \overline{\text{leave}_{i+1}} \cdot P_i$$



$$\text{Sys} = (F_1 | P_1 | F_2 | P_2 | F_3 | P_3 | F_4 | P_4 | F_5 | P_5) \setminus \{ \text{take}_i, \text{leave}_i \mid i = 1, \dots, 5 \}$$

Spec = ?

* Petersom's mutual exclusion

for two processes P_1 and P_2

shared variables - b_1, b_2 boolean

$b_i \equiv P_i$ wants to enter the critical section

- k with values 1 or 2

"turn variable"

process P_i ($i \in \{1, 2\}$, I use j for "the other value")

while true do

begin

<non critical code >

$b_i = \text{true}$

$k = j$

while (b_j and $k = j$) skip

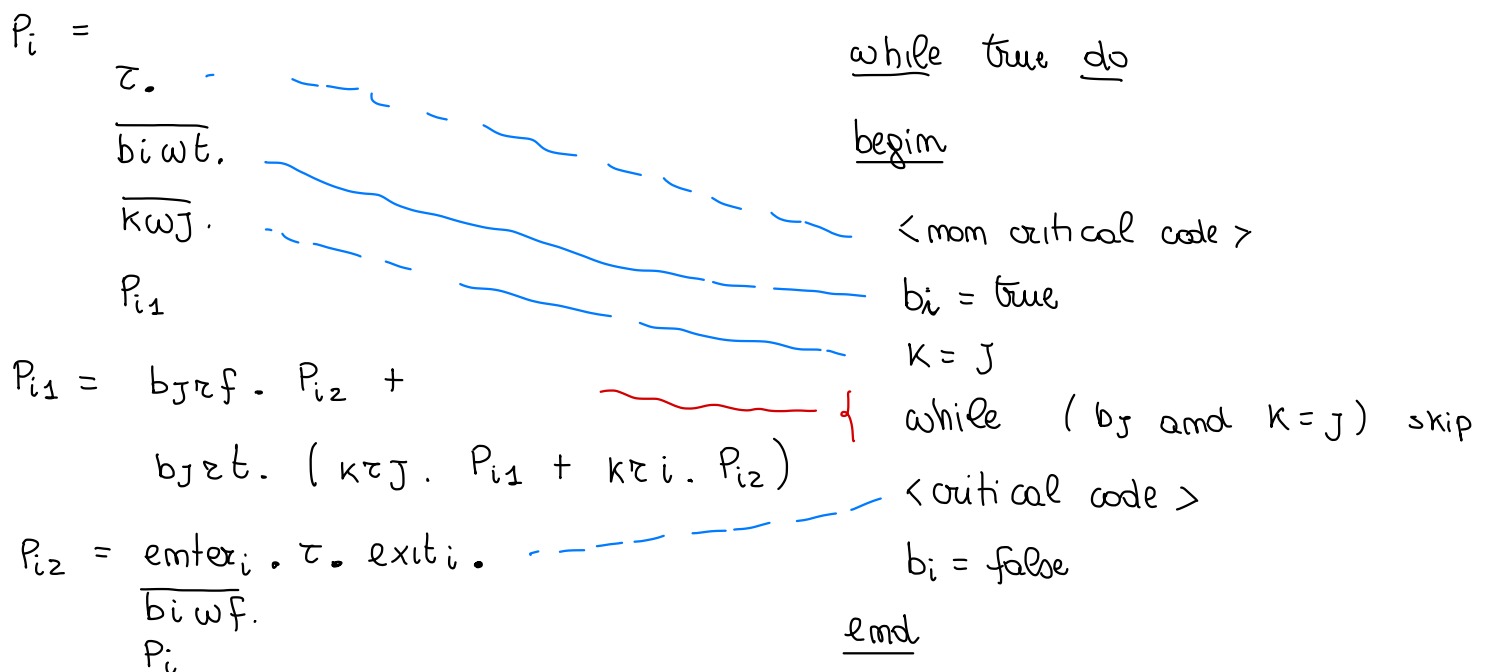
<critical code >

$b_i = \text{false}$

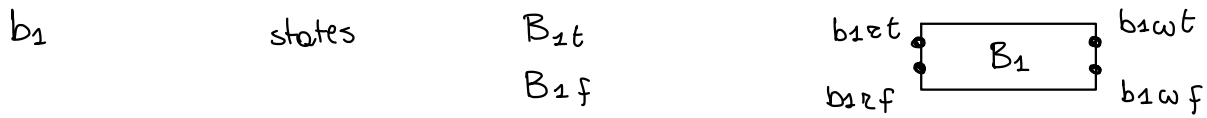
end

Is it really working? Does it ensure mutual exclusion?

CCS encoding?



How do we represent a variable? As a process!

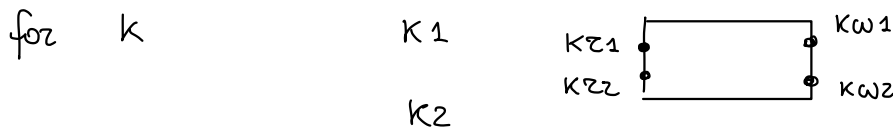


$$B_{1f} = \overline{b_{1rf}} \cdot B_{1f} + b_{1wt} \cdot B_{1t} + b_{1wf} \cdot B_{1f}$$

$$B_{1t} = \overline{b_{1rt}} \cdot B_{1t} + b_{1wt} \cdot B_{1t} + b_{1wf} \cdot B_{1f}$$

(if we had value passing

$$B_1(x) = \overline{b_{1r}(x)} \cdot B_1(x) + b_{1w}(y) \cdot B_1(y)$$



System

$$Sys = (P_1 | P_2 | B_1 | B_2 | K) \setminus \left\{ \begin{array}{l} \text{all channels} \\ \text{apart from enter;} \\ \text{exit} \end{array} \right\}$$

$$B_1 = \tau \cdot B_{1t} + \tau \cdot B_{1f}$$

$$B_2 =$$

$$K = \dots$$

← initial values

(Is $B_1 = B_{1t} + B_{1f}$ the same?)

2.7 Office : derive transition by using the rules

2.8 show that transitions exist or do not exist ...