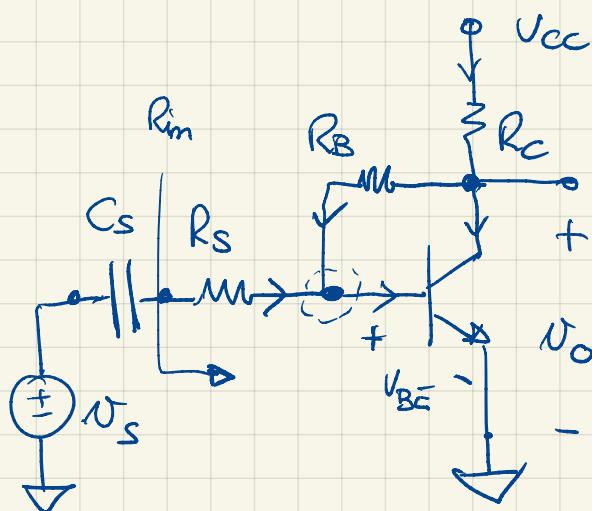


DISCUSS THE AMPLIFIER STABILITY.

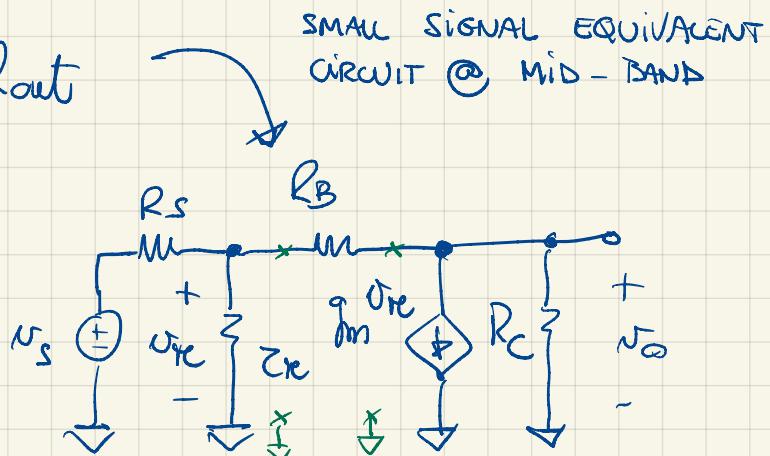
BUT WE CAN ALSO SHAPE THE B-NETWORK TO ENHANCE THE AMPLIFIER'S STABILITY MARGINS (PHASE MARGIN AND/OR GAIN MARGIN)

THIS IS CALLED COMPENSATION OF AN AMPLIFIER. THIS WILL BE THE OBJECT OF NEXT LESSONS.

EXAMPLE



CE AMPLIFIER WITH SELF-BIAS



1. FIND $A_V \triangleq \frac{V_O}{V_S}$, R_{in} AND R_{out}

2. DISCUSS THE POLE LOCATION

3. DRAW THE AMPLIFIER BLOCK DIAGRAM.

BIAS POINT ANALYSIS

$$V_{CC} = 5V \quad R_S = 100\Omega$$

$$I_C = 1mA$$

$$V_{BE} = 0.7V$$

$$I_B = 10\mu A$$

$$\beta_F = 100 \quad - \quad \beta_0 = 200$$

$$\text{DESIGN CHOICE } V_O = V_{CC}/2 \Rightarrow V_{RB} = V_O - 0.7V = 1.8V$$

$$R_B = \frac{V_{RB}}{I_B} = 180k\Omega$$

$$R_C = \frac{V_{CC} - V_O}{(\beta_F + 1)I_B} \approx 2.5k\Omega$$

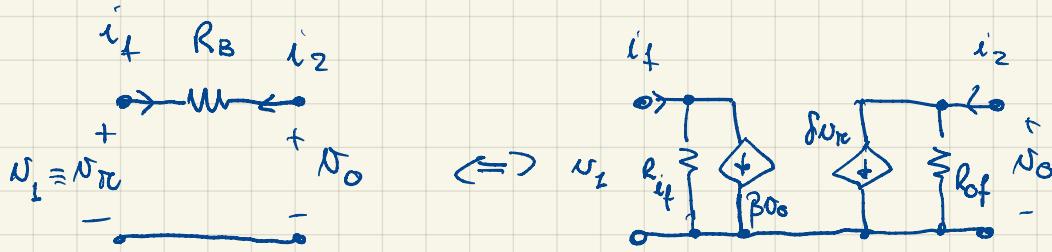
WITH CHOICES $V_{CE} = 2.5V > V_{CE,SAT}$ $I_C = 1mA$ F.A.R. BIAS ✓

$$g_m = \frac{I_c}{V_T} = 38 \cdot I_c = 38 \text{ mS}$$

$$V_T = 26 \text{ mV} @ T = 300 \text{ K}$$

$$r_{\text{fe}} = \frac{R_o}{g_m} \approx 5 \text{ k}\Omega$$

FEEDBACK TOPOLOGY : SHUNT - SHUNT (VOLTAGE SENSING, CURRENT MIXING) \Rightarrow TRANS-RESISTANCE AMPLIFIER

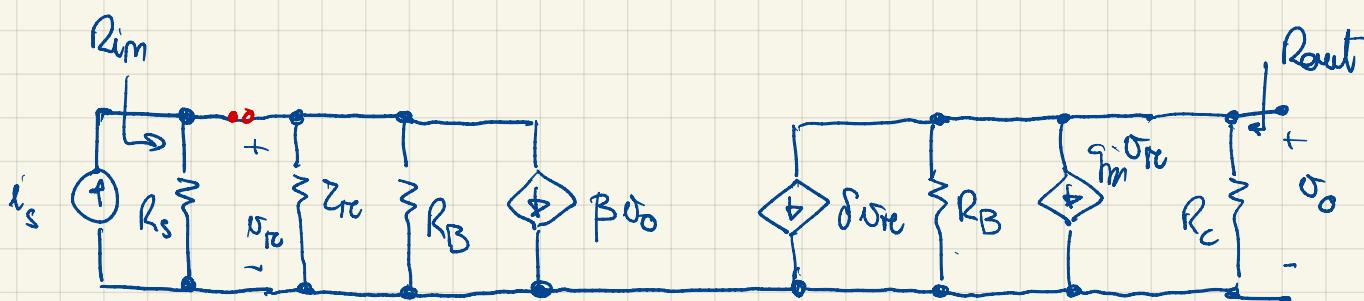


$$R_{if} = \frac{V_o}{i_f} \Big|_{V_o=0} = R_B$$

$$\beta = \frac{i_f}{V_o} \Big|_{V_i=V_{pi}=0} = -\frac{1}{R_B}$$

$$R_{of} = \frac{\infty}{i_2} \Big|_{V_i=N_{pi}=\emptyset} = R_B$$

$$\delta = \frac{i_2}{i_{if}} \Big|_{V_o=0} = -\frac{1}{R_B}$$



$$i_s = \frac{V_s}{R_s}$$

- UNILATERALITY $g_{im} = \frac{1}{R_B} \approx g_m$ if δ is NEGIGIBLE
 $\uparrow \approx 5 \mu\text{s}$
- OPEN LOOP PARAMETERS $(\beta=0, \delta \approx 0)$

$$A_{OL} = \frac{V_o}{V_s} = -(R_s \parallel R_B \parallel r_{fe}) \cdot g_m \cdot (R_B \parallel R_C) = -R_{im}^a g_m \cdot R_{out}^{OL}$$

$$R_{im}^a = R_s \parallel R_B \parallel r_{fe}$$

$$R_{out}^a = R_B \parallel R_C$$

o CLOSED LOOP PARAMETERS

$$A_F \triangleq \frac{A_\alpha}{1 + \beta A_\alpha} = - \frac{\frac{\alpha}{R_{in}} q_m \frac{\alpha}{R_{out}}}{1 - \frac{1}{R_B} \cdot (-\frac{\alpha}{R_{in}} q_m \frac{\alpha}{R_{out}})} = - \frac{\frac{\alpha}{R_{in}} q_m \frac{\alpha}{R_{out}}}{1 + q_m \frac{\alpha}{R_{in}} \cdot \frac{\alpha}{R_{out}}}$$

$$\approx - \frac{10^2 \cdot 38 \cdot 10^{-3} \cdot 2.5 \cdot 10^{-3}}{1 + \frac{10^4}{1.8 \cdot 10^5}} \approx - \frac{10^4}{1.05} \approx 9.5 \cdot 10^3 [\Omega]$$

$$R_{in}^F = R_{in}^{OL} / (1 + \beta A_\alpha) \approx 95 \Omega$$

$$R_{out}^F = R_{out}^{OL} / (1 + \beta A_\alpha) \approx 2.35 k\Omega$$

PARAMETER ADAPTATION

$$Q_{in}^l = \frac{1}{\frac{1}{R_{in}^F} - \frac{1}{R_s}} = \frac{1}{0.0105 - 0.01} = \frac{1}{0.0005} = 2 \cdot 10^3 \Omega$$

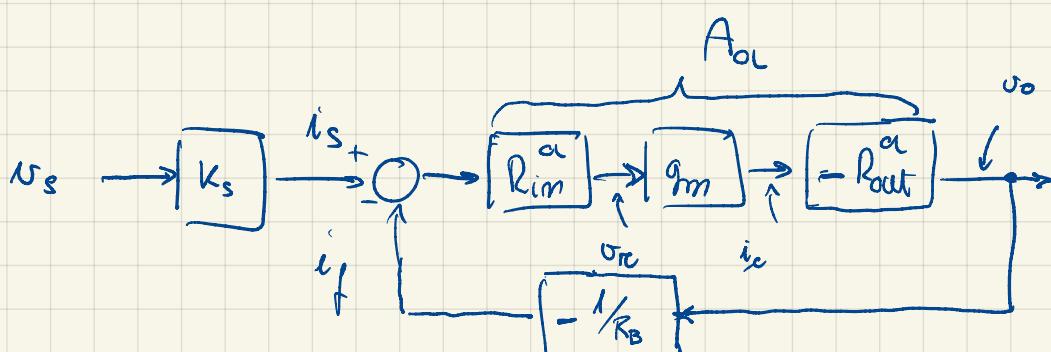
$$R_{in} = R_s + R_{in}^l = 2.1 k\Omega$$

$$R_{out} = R_{out}^F$$

k_s INPUT SCALING FACTOR.

$$A_V = \frac{V_o}{V_s} = \frac{V_o}{i_s} \cdot \frac{i_s}{V_s} = A_F \cdot \frac{1}{R_s}$$

BLOCK DIAGRAM OF THE AMPLIFIER



o PASE LOCATION (considering only $C_S \Rightarrow C_R$ AND C_μ NEGIGIBLE)

$$R_{CS} = R_{in} = R_s + R_m' = 2.1 \text{ k}\Omega$$

$$Z_{CS} = G_S \cdot R_{CS} \Rightarrow \omega_{CS} = \frac{1}{Z_{CS}} = f_p = \frac{\omega_{CS}}{2\pi}$$

$$R_{CS}^a = R_s + Z_{TC}/(R_B) \approx 4.9 \text{ k}\Omega$$

FEED BACK MADE R_{CS} SMALLER $\Rightarrow Z_{CS}$ BECOMES SMALLER
 $\Rightarrow \omega_p$ IS LARGER!!

NO BANDWIDTH WIDENING IS FOUND.

EXERCISE #1

Consider the amplifier in the figure, whose parameters at $T = 25^\circ\text{C}$ are the following:
 $V_{CC} = 24\text{V}$, $R_2 = 500\text{k}\Omega$, $R_3 = 3\text{k}\Omega$, $R_S = 100\text{k}\Omega$

Q1: $I_{C1} = 0.6\text{mA}$, $V_{BE} = 0.7\text{V}$, $\beta_F = 50$, $\beta_0 = 80$

Q2: $V_{BE} = 0.7\text{V}$, $\beta_F = 50$, $\beta_0 = 80$

Considering C_1 a short circuit at mid-band, determine:

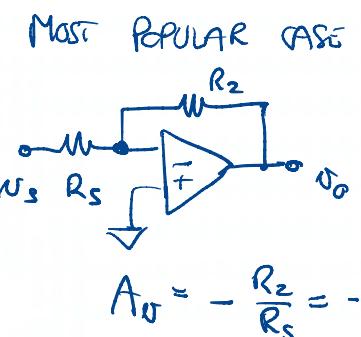
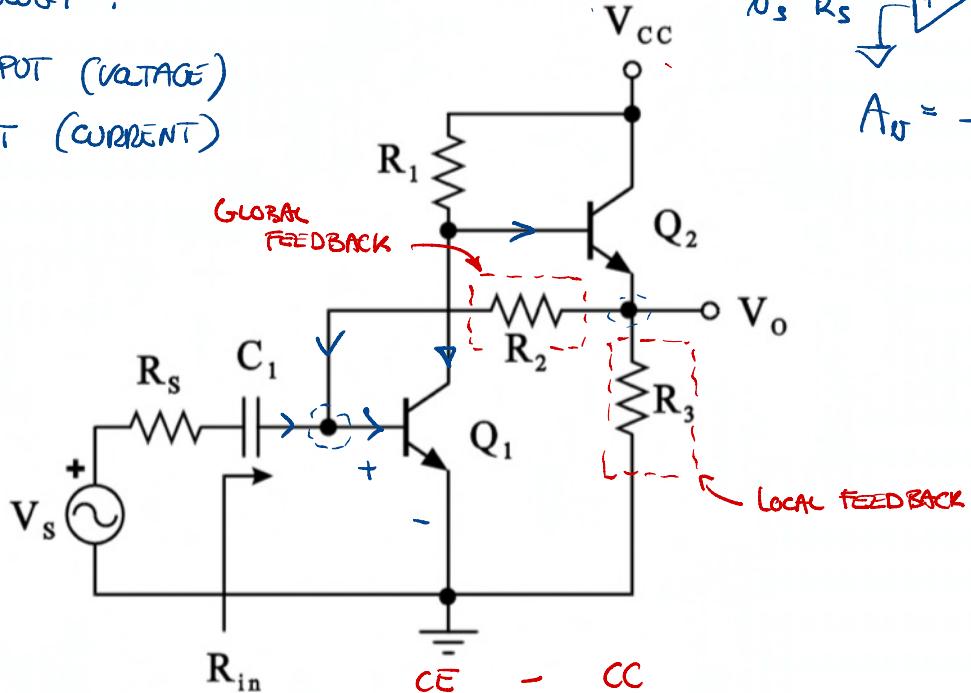
- 1) The bias values of I_{C2} , V_{CE1} , V_{CE2} and the value of R_1 ; ←
- 2) The type of feedback and the value of β ;
- 3) The open loop amplifier gain A_{OL} ;
- 4) Voltage gain $A_v = V_o/V_s$
- 5) Input resistance R_{in} .

FEEDBACK TOPOLOGY ?

SHUNT @ OUTPUT (VOLTAGE)

SHUNT @ INPUT (CURRENT)

$A_{OL} = -5$?



1. BIAS POINT ANALYSIS

$$I_{B2} = \frac{I_{C2}}{\beta_F} = 12 \mu\text{A}$$

$$V_o = V_{BE2} + R_B \cdot I_{B2} = 0.7\text{V} + 12 \cdot 10^{-6} \cdot 5 \cdot 10^3 = 6.7\text{V}$$

$$V_{CE2} = V_{CC} - V_o = 24 - 6.7 = 17.3\text{V} \gg V_{CE,SAT}$$

$$I_{R_3} = \frac{V_0}{R_3} = \frac{6.7}{3k} = 2.23 \text{ mA}$$

$$I_{E_2} = I_{R_3} + I_{B_1} = 2.242 \text{ mA}$$

$$I_{B_2} = \frac{I_{E_2}}{\beta_{F_2} + 1} = \frac{2.24}{51} = 44.8 \mu\text{A}$$

$$I_{R_1} = I_{B_2} + I_{C_1} = 0.6 \text{ mA} + 45 \mu\text{A} = 0.645 \text{ mA}$$

$$V_{B_2} = V_0 + V_{B_2^-} = 7.4 \text{ V} = V_{C_1^-} \gg V_{C_1^- S A T_2} = 0.2 \text{ V}$$

$$V_{R_1} = V_{cc} - V_{B_2} = 24 - 7.4 = 16.6 \text{ V}$$

$$R_1 = \frac{V_{R_1}}{I_{R_1}} = \frac{16.6}{0.645} \cdot 10^3 \approx 25.7 \text{ k}\Omega$$

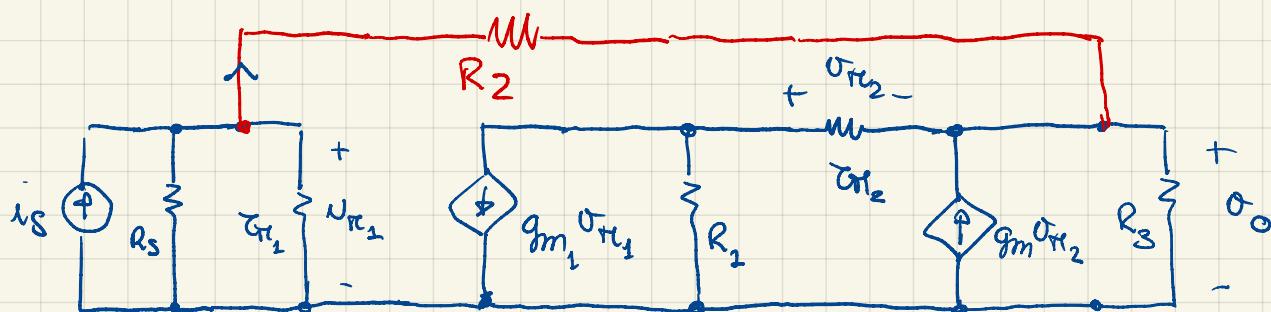
BOTH TRANSISTORS ARE BIASED IN THE FORWARD ACTIVE REGION.

SMALL SIGNAL CIRCUIT PARAMETERS

$$g_{m_1} = \frac{I_{C_1}}{V_T} \approx 25 \text{ mS} \quad g_{m_2} = \frac{I_{C_2}}{V_T} \approx 85 \text{ mS}$$

$$Z_{T_{V_1}} = \frac{R_{o_1}}{g_{m_1}} \approx 3.25 \text{ k}\Omega \quad Z_{T_{V_2}} = \frac{R_{o_2}}{g_{m_2}} = 946 \text{ }\Omega$$

2.

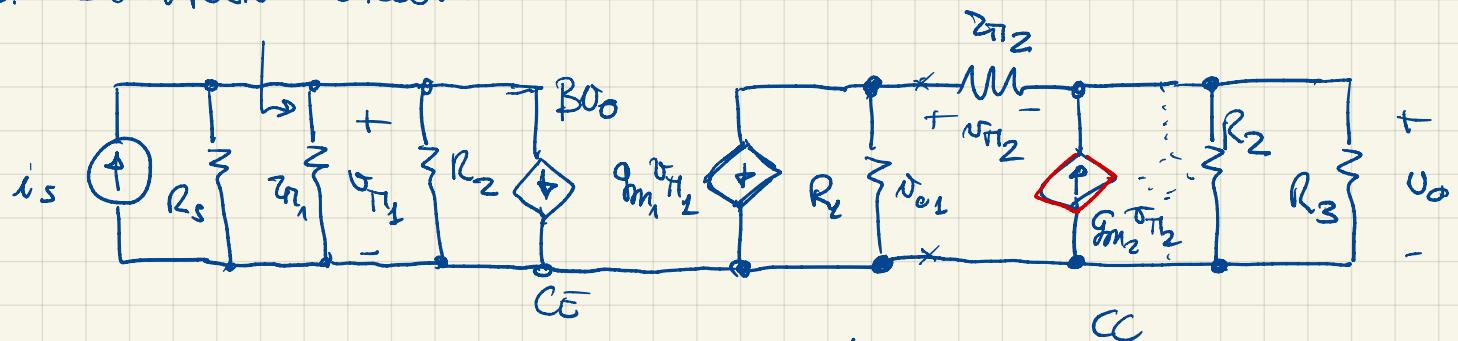


$$i_S = \frac{v_S}{R_s}$$

FEEDBACK TOPOLOGY: SHUNT-SHUNT \rightarrow

TRANSRESISTANCE AMPLIFIER

3. EQUIVALENT CIRCUIT



NEGLECTING δ AND WITH $\beta = -\frac{1}{R_2}$

OPEN LOOP PARAMETERS $\beta = 0$

$$\frac{V_o}{i_s} = A_{OL} = - \left(R_s \parallel R_2 \parallel Z_{T1} \right) g_{m1} \cdot \underbrace{\left(R_1 \parallel R_{in2}^{CC} \right)}_{24 \text{ mS}} \cdot \underbrace{\frac{(\beta_0 + 1) R_2 \parallel R_3}{Z_{T2} + (\beta_0 + 1) R_2 \parallel R_3}}_{\approx 1} \approx -1.9 \text{ M}\Omega$$

$$R_{in2}^{CC} = Z_{T2} + (\beta_0 + 1) R_2 \parallel R_3 \approx 242 \text{ k}\Omega$$

$$A_F = \frac{A_a}{1 + \beta A_a} = \frac{1.9 \cdot 10^6}{1 + 3.8} \approx -396 \text{ k}\Omega \quad (\text{DIFFERS FROM } -R_B)$$

$\curvearrowleft T \text{ IS NOT } \gg 1$

4.

$$A_D = \frac{N_o}{N_s} = \underbrace{\frac{V_o}{i_s}}_{A_F} \cdot \underbrace{\frac{i_s}{N_s}}_{R_s} = \frac{A_F}{R_s} - 3.96 \quad (\text{INSTEAD OF } -S)$$

$$A_F \quad N_s = \frac{1}{R_s}$$

5.

$$R_{in} = \frac{1}{\frac{1}{R_{in}^F} - \frac{1}{R_s}} \approx 706 \text{ }\Omega \quad \text{with} \quad R_{in}^F = \frac{R_{in}}{1 + \beta A_a}$$

HOMEWORK

EXERCISE #2

Consider the two stage amplifier in the figure below. The circuit parameters at $T=25^\circ\text{C}$ are the following:

$$V_{CC} = 24 \text{ V}, R_S = 0.6 \text{ k}\Omega, R_1 = 300 \text{ k}\Omega, R_2 = 270 \text{ k}\Omega, R_3 = 4.7 \text{ k}\Omega, R_F = 10 \text{ k}\Omega$$

$$Q_1: I_{DSS} = 1 \text{ mA}, V_T = 5 \text{ V}, r_d = \infty$$

$$Q_2: I_{CQ} = -2 \text{ mA}, V_{CEQ} = -8 \text{ V}, V_{BE} = -0.65 \text{ V}, \beta_F = \beta_0 = 100, r_o = \infty$$

Determine:

- 1) the values of resistances R_4 e R_L ;
- 2) the type of feedback;
- 3) the feedback gain β ;
- 4) the open loop amplifier gain A_{OL} ;
- 5) the closed loop amplifier gain A_F ;
- 6) the voltage gain of the circuit $A_V = V_O/V_S$;
- 7) the indicated output resistance R_{OUT} .

