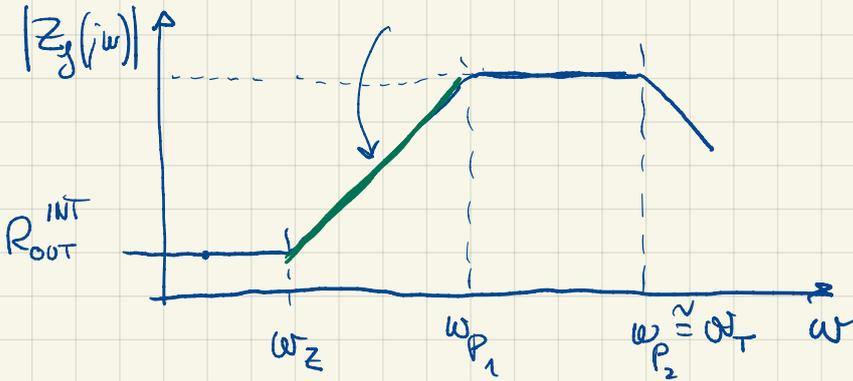


$$Z_y(s) = R_{out}^{INT} \cdot \frac{1 + \frac{s}{\omega_z}}{\left(1 + \frac{s}{\omega_{p1}}\right)\left(1 + \frac{s}{\omega_{p2}}\right)}$$

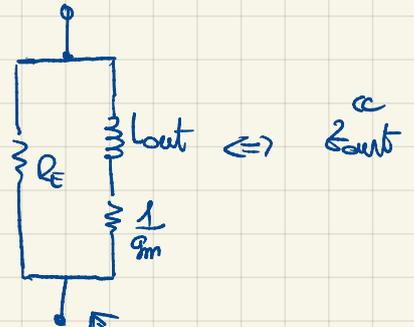
IT IS POSSIBLE THAT  $\omega_z < \omega_{p1} < \omega_{p2} \approx \omega_T$

INDUCTIVE REGION!

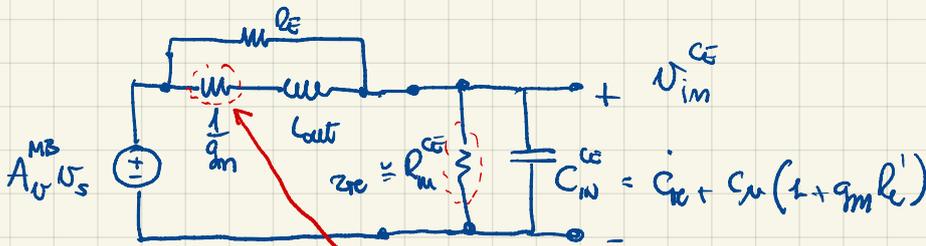


IN THE INDUCTIVE REGION

$$\omega < \omega_{p1}$$



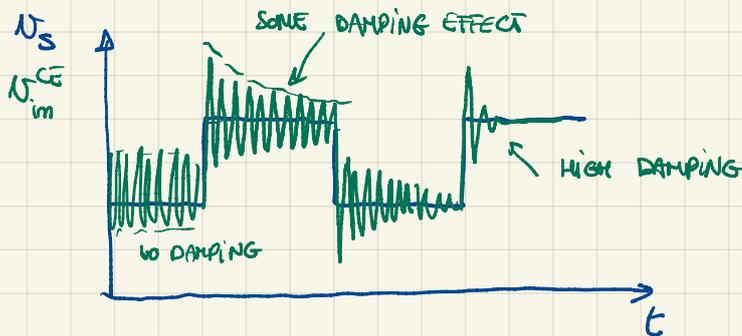
THIS CAN BE A PROBLEM FOR C.C. + C.E STAGES (BUFFERED COMMON EMITTER).



EQUIVALENT CIRCUIT REPRESENTING THE CC AMPLIFIER OUTPUT IMPEDANCE FOR  $\omega < \omega_{p1}$

EQUIVALENT TRENKLEIN MODEL OF C.C. STAGE

$$\zeta \propto \frac{1}{g_m}, \frac{1}{\zeta C} \text{ IS RELATIVELY SMALL}$$

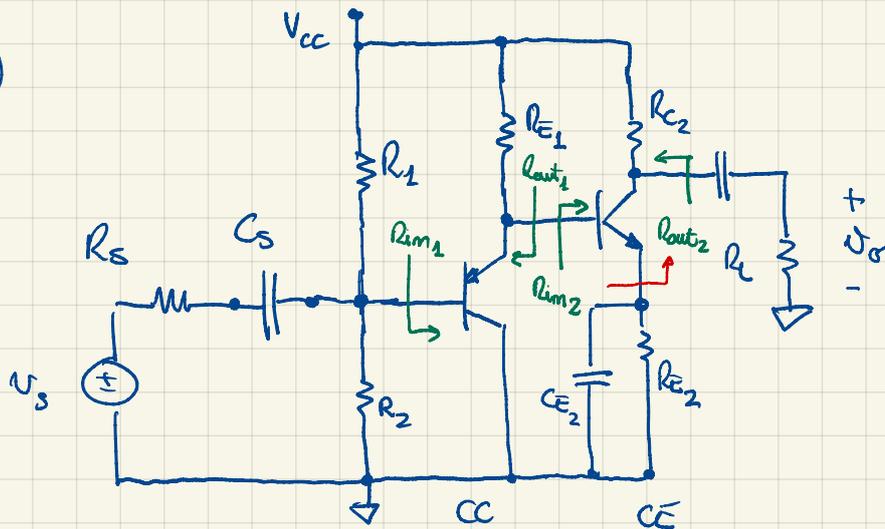


$$\omega_R \approx \frac{1}{\sqrt{C_{out} C_{IN}^{CE}}}$$

# FREQUENCY RESPONSE OF MULTI-STAGE AMPLIFIER

1. BUFFERED CE STAGE (CC + CE)
2. CASCODE STAGE (CE + CB)

1.



## TO DO LIST

# FIND THE BIAS POINTS

$$Q_1: (V_{CE1}, I_{E1})$$

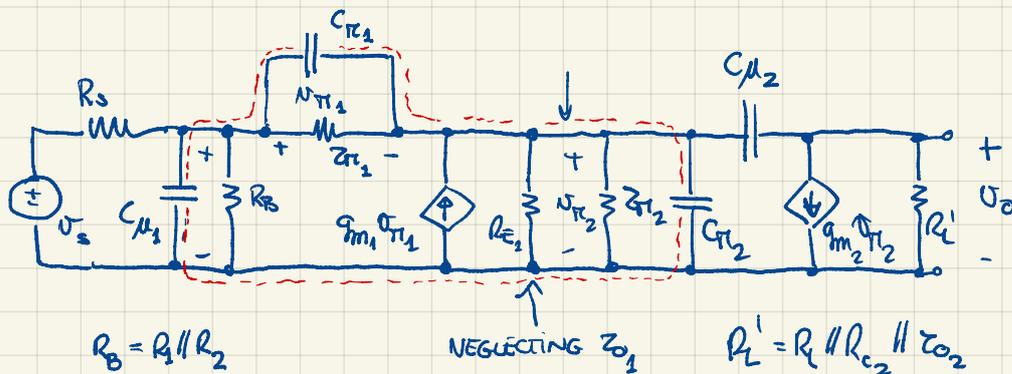
$$Q_2: (V_{CE2}, I_{C2})$$

# VERIFY THE BIAS POINTS FALL INTO THE FORWARD ACTIVE REGION

# CALCULATE (OR FIND) THE SMALL SIGNAL PARAMETERS  $z_{\pi}, g_m, C_{\pi}, z_o, \dots$

# SOLVE THE SMALL SIGNAL CIRCUIT TO FIND:

NB  $A_v, z_{in}, z_{out}, \omega_L, \omega_H$



$$R_B = R_1 || R_2$$

NEGLECTING  $z_{o 1}$

$$R_L' = R_L || R_{C2} || z_{o 2}$$

$$R_{in1} = R_{in_{CC}}^{INT} = z_{\pi 1} + (\beta_0 + 1) R_{E1} || R_{in2} \quad \text{LOADING EFFECT!}$$

$$R_{out1} = R_{out_{CE}}^{INT} = R_{E1} || \frac{R_s || R_B + z_{\pi 1}}{\beta_0 + 1} \approx \frac{1}{g_{m1}}$$

$$R_{in2} = R_{in_{CE}}^{INT} = z_{\pi 2} \approx \frac{1}{g_{m2}}$$

$$R_{out2} = R_{out_{CB}}^{INT} = R_{C2} || z_{o 2}$$

$$A_v^{NB} = \frac{U_o}{U_s} = -g_{m2} R_L' \cdot \frac{(\beta_0 + 1) R_{E1} || R_{in2}}{z_{\pi 1} + (\beta_0 + 1) R_{E1} || R_{in2}} \cdot \frac{R_B || R_{in1}}{R_s + R_B || R_{in1}}$$

$\omega_L \rightarrow$  USE OF SCFC METHOD UNDER DOMINANT POLE ASSUMPTION.

$$\omega_L \approx \frac{1}{C_S R_S} + \frac{1}{C_{E_2} R_{E_2}} + \frac{1}{C_L R_L}$$

$$R_S^{SC} = R_S + R_B \parallel R_{in1} \quad \text{LARGE} \quad 10^5 \Omega$$

$$R_{E_2}^{SC} = R_{E_2} \parallel \frac{r_{\pi 2} + R_{out1}}{\beta_2 + 1} \approx R_{E_2} \parallel \frac{1}{g_{m2}} \quad \text{SMALL} \quad \leftarrow \text{DOMINANT } 10^1 \Omega$$

$$R_L^{SC} = R_L + R_{out2} \quad \text{MEDIUM} \quad 10^3 \Omega$$

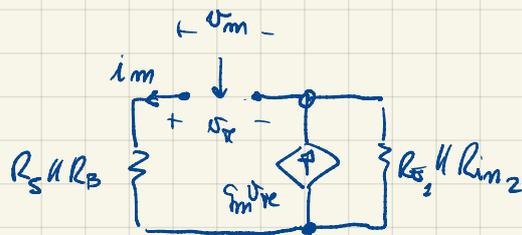
•  $\omega_H$  USING OCTC METHOD AND DOMINANT POLE ASSUMPTION

$$\omega_H \approx \frac{1}{\underbrace{C_{\mu 1} R_{\mu 1}^0 + C_{\mu 2} R_{\mu 2}^0}_{a_{11} \text{ WITH LOADING EFFECTS}} + \underbrace{C_{\pi 2} R_{\pi 2}^0 + C_{\mu 2} R_{\mu 2}^0}_{a_{12} \text{ WITH LOADING EFFECT}}} = \frac{1}{\frac{1}{\omega_{H1}} + \frac{1}{\omega_{H2}}}$$

$$\omega_H < \min(\omega_{H1}, \omega_{H2}) \quad \text{BUT} \quad \omega_H > \omega_{HCE} \text{ UNLOADED!}$$

LET'S SEE WHY!

$$R_{\mu 1}^0 = r_{\pi 1} \parallel \frac{R_S \parallel R_B + R_{E_1} \parallel R_{in2}}{1 + g_{m1} (R_{E_1} \parallel R_{in2})}$$



$$v_{be} = (R_S \parallel R_B) i_{in} + R_{E_1} \parallel R_{in2} (i_{in} - g_m v_{be})$$

$$R_{\mu 1}^0 = R_S \parallel R_B \parallel R_{in1} = R_S \parallel R_B \parallel [r_{\pi 1} + (\beta_1 + 1) R_{E_1} \parallel R_{in2}]$$

LOADING EFFECT

THE LOADING EFFECT OF THE C.E. STAGE OVER THE C.C. STAGE IS NOT STRONG BECAUSE TYPICALLY  $r_{\pi} > R_E$  !!

$$a_{11} \approx a_{11}^{CC} \quad \text{WITH NO LOAD}$$

LOADING EFFECT MAKES  $R_{\pi 2}^0$  SMALL

$$R_{\pi 2}^0 = r_{\pi 2} \parallel R_{out1} \approx r_{\pi 2} \parallel \left( \frac{1}{g_{m1}} \right) \quad \text{SMALL!}$$

$$R_{\mu 2}^0 = R_{H2}^0 + R_L' \cdot (1 + g_{m2} R_{H2}^0) = R_L' + R_{\pi 2}^0 (1 + g_{m2} R_L')$$

THANKS TO THE LOADING EFFECTS  $a_{12} \ll a_1^{CE}$  AS THE MILLER MULTIPLIER IS STRONGLY REDUCED.

AS A RESULT  $\omega_{H2} \gg \omega_H^{CE} \Rightarrow \omega_H > \omega_H^{CE} !!$

THE BUFFER STAGE ALLOWS TO MAKE THE AMPLIFIER BANDWIDTH LARGER.

N.B. DO NOT FORGET THE POTENTIAL RESONANCE BETWEEN  $Z_{in}^{CE}$  AND  $Z_{out}^{CC}$

ADDITIONAL DETAILS:  $M=3$  EVEN IF  $N=4$  AT HIGH FREQUENCY, BECAUSE WE HAVE A LOOP INVOLVING  $C_{T1}, C_{\mu 1}, C_{T2}$ .

- BESIDES,  $N_0$  GOES TO ZERO LIKE  $\frac{1}{s}$  AS  $s \rightarrow \infty$  WHICH MEANS  $m=2$ .  
NOTICE THE CAPACITIVE VOLTAGE DIVIDER MADE UP BY  $C_{T1}$  AND  $C_{T2}$ !

- THE TWO ZEROS CAN BE FOUND AS IN THE SINGLE STAGE CE AND CC AMPLIFIER.